Electronic Acknowledgement Receipt FES ID: 1101429 Application Number: 10743079 Confirmation Number: 8111 Input/output architecture for integrated circuits with efficeint positioning Title of Invention: of integrated circuit elements First Named Inventor: Uming Ko Customer Number: 23494 Filer: Yingsheng Tung/Jackie McBride Filer Authorized By: Yingsheng Tung Attorney Docket Number: TI-29632.1 Receipt Date: 03-JUL-2006 Filing Date: 23-DEC-2003 Time Stamp: 15:51:39 Application Type: Utility International Application Number: Payment information:

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1	Information Disclosure Statement (IDS) Filed	TI-29632_1_US_IDS_Form. pdf	805674	no	4

Warnings:					
Information:					
2	NPL Documents	0660410A2.PDF	277318	no	6
Warnings:					
Information:					
3	NPL Documents	ARTICLE_1.PDF	469219	no	5
Warnings:					
Information:					
4	NPL Documents	ARTICLE_2.PDF	598962	no	7
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